

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3490	(semiconductor or die or ic or chip or (integrated adj circuit)) and (sti) and (pwell or nwell or (p near2 well) or (n near2 well))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 13:33
L2	1606	(semiconductor or die or ic or chip or (integrated adj circuit)) and ((sti) or (shallow near2 trench near2 insulator)) and (cavity or void or pocket)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 13:33
L3	3031	1 not L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 13:33
S1	1	("6630714").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:01
S2	1	("5320975").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:01
S3	1	("5172203").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:01
S4	1	("6121662").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:01
S5	1	("6358828").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:02
S6	1	("6756257").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:02
S7	1	("6472753").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:02
S8	1	("6620659").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:02
S9	1	("6271542").PN.	USPAT; USOCR	OR	OFF	2004/11/01 13:02
S10	8	((("5320975").PN.) or (("5172203").PN.) or (("6121662").PN.) or (("6358828").PN.) or (("6756257").PN.) or (("6472753").PN.) or (("6620659").PN.) or (("6271542").PN.))	USPAT	OR	OFF	2004/11/01 13:05
S11	441	257/499.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 15:22

S12	344	(semiconductor or die or ic or chip or (integrated adj circuit)) and (sti) and cavity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/25 10:32
S13	167	(semiconductor or die or ic or chip or (integrated adj circuit)) and (sti) and son	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/03 20:13
S14	2522	(semiconductor or die or ic or chip or (integrated adj circuit)) and (sti) and (pwell or nwell or (p near2 well) or (n near2 well))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 13:32
S15	1370	"257"/\$6.ccls. and ((semiconductor or die or ic or chip or (integrated adj circuit)) and (sti) and (pwell or nwell or (p near2 well) or (n near2 well)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/03 20:15
S24	1080	(semiconductor or die or ic or chip or (integrated adj circuit)) and ((sti) or (shallow near2 trench near2 insulator)) and (cavity or void)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/25 12:39
S25	432	(semiconductor or die or ic or chip or (integrated adj circuit)) and ((sti) or (shallow near2 trench near2 insulator)) and (pocket)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/25 12:39
S26	1080	(semiconductor or die or ic or chip or (integrated adj circuit)) and ((sti) or (shallow near2 trench near2 insulator)) and (cavity or void)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 11:45
S27	390	S25 not S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/25 12:40
S28	489	257/499.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 15:26
S29	1137	257/506.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 15:26

S30	316	257/522.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 15:26
S31	1866	S28 S29 S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 15:26
S32	1606	(semiconductor or die or ic or chip or (integrated adj circuit)) and ((sti) or (shallow near2 trench near2 insulator)) and (cavity or void or pocket)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/15 11:45